

ABSTRACT OF THE DISCLOSURE

Disclosed is a printed circuit board which is guaranteed to be free of defective soldering in mounting and connecting electric or electronic parts or devices to the printed circuit. The circuit board has a circuit pattern printed on its substrate, and an anti-soldering layer is laid on the circuit pattern to prevent soldering material from sticking to the circuit pattern and silk-screen printing areas are laid on the anti-soldering layer to indicate where selected electric or electronic parts or devices are to be mounted. Each silk-screen printing area has terminal holes made therein. The board has a substrate-exposed zone traversing the silk-screen printing area to leave its opposite extensions open to the surrounding atmosphere, thereby allowing heated air and gases to escape from the interspace between the bottom of the electric or electronic part or device and the exposed substrate.

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